

S/N: -----
 Docket No.: UMI-360
 Title: HIGH SPEED POWER-GATING TECHNIQUE
 FOR INTEGRATED CIRCUIT DEVICES
 INCORPORATING A SLEEP MODE OF OPERATION
 Inv: Kim C. Hardee

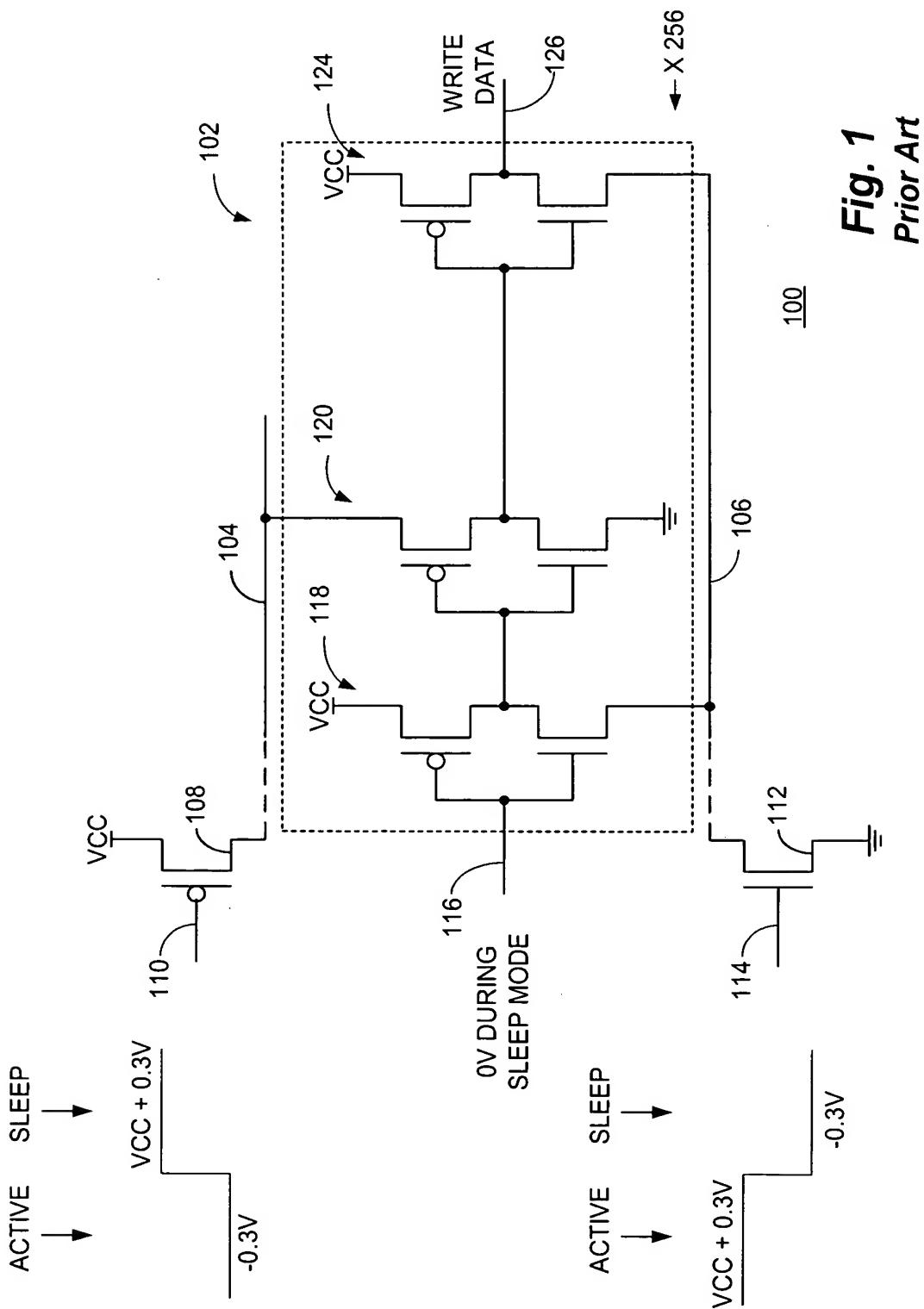


Fig. 1
Prior Art

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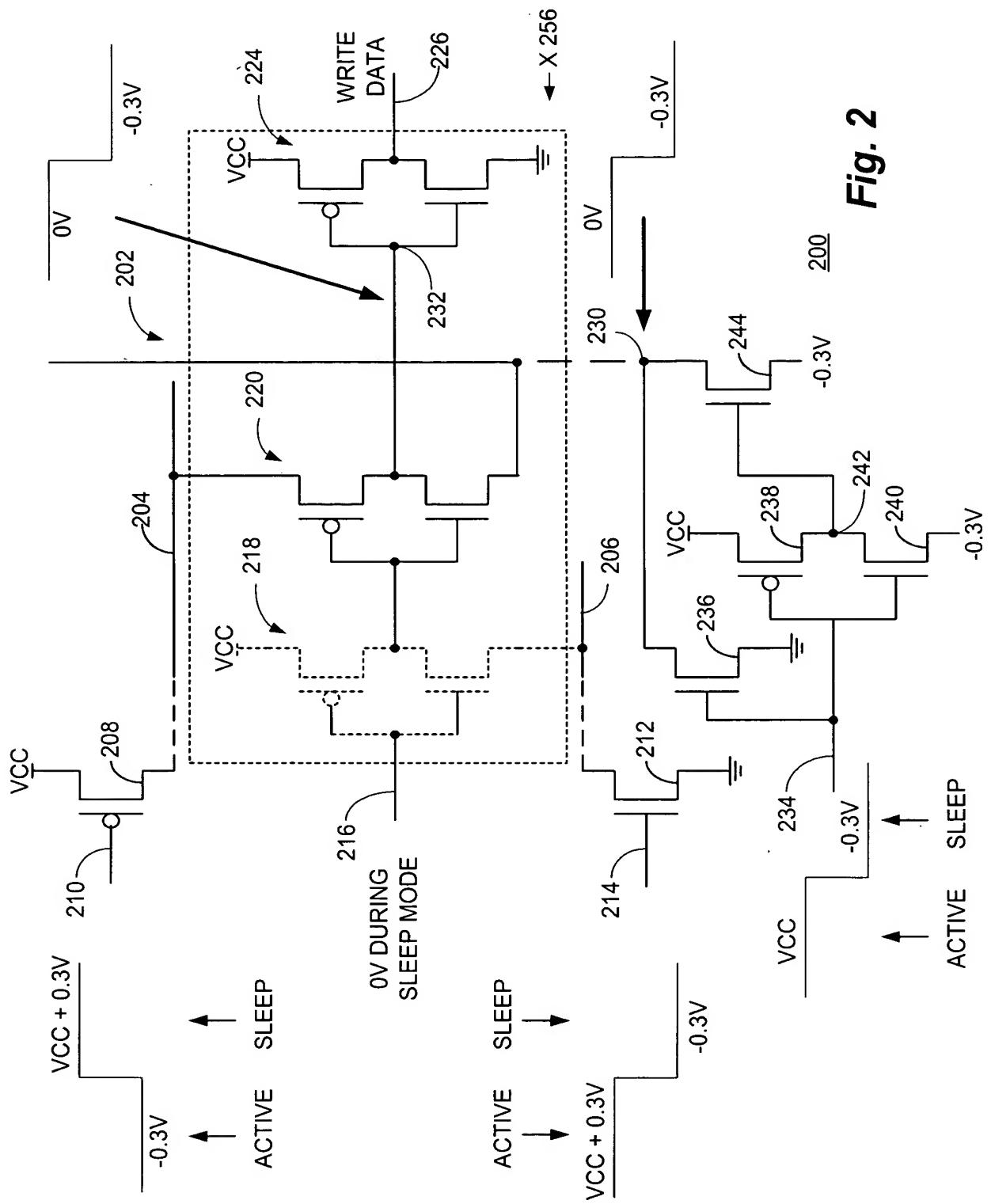


Fig. 2